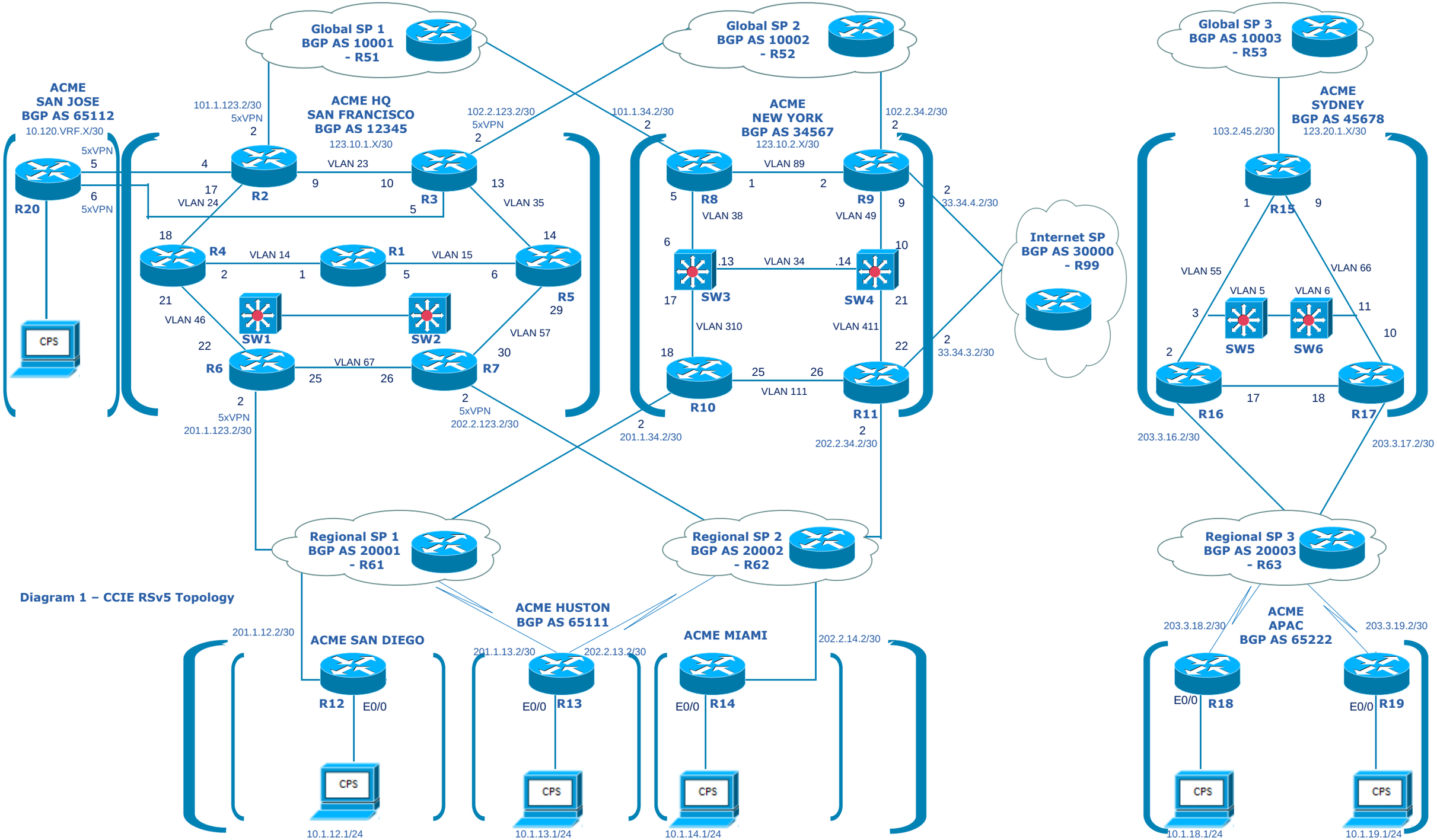

Diagram 1 – CCIE RSv5 Topology